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03/01/2005

HEWLETT-PACKARD COMPANY
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Be Henry (Depositor's name)
Be Henry (Signature)
3/28/2005 (Date)

APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/698,130	10/31/2003	Luiz A. Barros	200302253-2	2813

TITLE OF INVENTION: MULTIPROCESSOR CACHE COHERENCE SYSTEM AND METHOD IN WHICH PROCESSOR NODES AND INPUT/OUTPUT NODES ARE EQUAL PARTICIPANTS

APPLN. TYPE	SMALL ENTITY	ISSUE FEE	PUBLICATION FEE	TOTAL FEE(S) DUE	DATE DUE
nonprovisional	NO	\$1400	\$300	\$1700	06/01/2005

EXAMINER	ART UNIT	CLASS-SUBCLASS
NGUYEN, THAN VINH	2187	711-141000

1. Change of correspondence address or indication of "Fee Address" (37 CFR 1.363).

- ☐ Change of correspondence address (or Change of Correspondence Address form PTO/SB/122) attached.
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- (1) the names of up to 3 registered patent attorneys or agents OR, alternatively,
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3. ASSIGNEE NAME AND RESIDENCE DATA TO BE PRINTED ON THE PATENT (print or type)

PLEASE NOTE: Unless an assignee is identified below, no assignee data will appear on the patent. If an assignee is identified below, the document has been filed for recordation as set forth in 37 CFR 3.11. Completion of this form is NOT a substitute for filing an assignment.

(A) NAME OF ASSIGNEE

(B) RESIDENCE: (CITY and STATE OR COUNTRY)

Hewlett-Packard Development Company, L.P.

Houston, Texas

Please check the appropriate assignee category or categories (will not be printed on the patent): ☐ Individual ☒ Corporation or other private group entity ☐ Government

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- ☒ Issue Fee
☒ Publication Fee (No small entity discount permitted)
☐ Advance Order - # of Copies

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- ☐ A check in the amount of the fee(s) is enclosed.
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- ☐ a. Applicant claims SMALL ENTITY status. See 37 CFR 1.27. ☐ b. Applicant is no longer claiming SMALL ENTITY status. See 37 CFR 1.27(g)(2).

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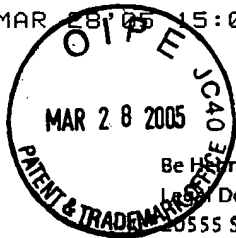
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PTOL-85 (Rev. 12/04) Approved for use through 04/30/2007.

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03/30/2005 AWONDAF2 00000224 082025 10698130

01 FC:1501 1400.00 DA
02 FC:1504 300.00 DA



Be Henry
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Hewlett-Packard Co.

Fax: USPTO-Box Issue Fee

To: U.S. Patent and Trademark Office	From: Howard Boyle
Fax: 703-746-4000	Pages: 3
Phone: 703-305-8189	Date: 3/28/05
Re: 10/698,130 (200302253-2)	Title: Multiprocessor Cache Coherence System And Method In Which Processor Nodes And Input/Output Nodes Are Equal Participants

☐ Urgent ☒ For Review ☐ Please Comment ☐ Please Reply ☐ Please Recycle

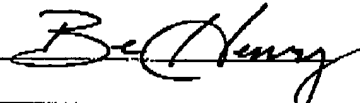
• **Comments:**

Ladies and Gentlemen:

Transmitted herewith is a Part B-Fee(s) Transmittal, in duplicate, authorizing payment from Deposit Account No. 08-2025.

- \$1,700 issue fee
- \$ 300 Publication fee

For further questions, please contact Be Henry at 281-518-5992.

<i>CERTIFICATE OF TRANSMISSION</i>	
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Be Henry	

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